

**AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (previously presented) A package assembly for an electronic device, comprising:
  - a substrate having a first surface with a first plurality of contact pads and a second plurality of contact pads, a second surface with a plurality of connection pads, and a plurality of via holes connecting said first plurality of contact pads and said plurality of connection pads; and
  - a buffer layer between said substrate and said electronic device, and a surface of said electronic device having electrodes being opposite to said first surface of said substrate, said buffer layer having an opening to expose said first plurality of contact pads, wherein said buffer layer has a first part with a first density and a second part with a second density, said first density greater than said second density, wherein said second part of said buffer layer surrounds the edge of said electronic device and said first part of said buffer layer is configured with said electronic device such that said buffer layer functions as a self-planarization buffer between said electronic device and said substrate for increasing the hermeticity thereof.
2. (currently amended) The package assembly in claim 1, wherein said substrate is selected from the group consisting of an aluminum  $\text{Al}_2\text{O}_3$  substrate, a ceramic substrate, a silicon substrate, a polymer substrate, and a glass substrate.
3. (original) The package assembly in claim 1, wherein said buffer layer is selected from the group consisting of an organic film layer and a polymer film layer.
4. (cancelled)
5. (cancelled)

6. (cancelled)

7. (currently amended) The package assembly in claim 1, further ~~comprises~~ comprising a conductive layer formed on said electronic device with respect to said lower surface.

8. (currently amended) The package assembly in claim 1, wherein said ~~buffer layer~~ first part has a relatively greater thickness and said second part has a relatively smaller thickness of between 30-200 microns.

9. (currently amended) The package assembly in claim 1, wherein said ~~fastening face of said edge of said electronic device and said buffer layer~~ first part is higher than said lower surface of said electronic device in altitude, contacted closely with said electronic device and disposed horizontally outside said electronic device.

10. (cancelled)

11. (original) The package assembly in claim 1, wherein said electronic device is a surface acoustic wave device.

Claims 12 – 21 (cancelled)

22. (New) A package assembly for an electronic device having a lower surface having electrodes thereon, an upper surface and a vertical side with respect to said lower surface, comprising:

a substrate having a first surface with a first plurality of contact pads and a second plurality of contact pads, a second surface with a plurality of connection pads, and a plurality of via holes connecting said first plurality of contact pads and said plurality of connection pads; and

a buffer layer disposed on said substrate and having an opening to expose said first plurality of contact pads, wherein said buffer layer further has a first part and a second part, said first part being lower than said upper surface of said electronic device and higher than said lower surface of said electronic device in altitude.

23. (New) A package assembly for an electronic device having a lower surface having electrodes thereon, an upper surface and a vertical side with respect to said lower surface, comprising:

a substrate having a first surface with a first plurality of contact pads and a second plurality of contact pads, a second surface with a plurality of connection pads, and a plurality of via holes connecting said first plurality of contact pads and said plurality of connection pads; and

a buffer layer disposed substantially between said substrate and said electronic device and having an opening to expose said first plurality of contact pads, wherein said buffer layer further forms a shoulder with a first surface disposed alongside said vertical side of said electronic device and being contacted closely with said vertical side and a second surface bearing said electronic device at said lower surface and being contacted closely with said lower surface.

24. (New) The package assembly for an electronic device according to claim 23, wherein said first surface being higher than said second surface in altitude and integrally formed with each other.

25. (New) A package assembly for an electronic device having a lower surface having electrodes thereon, an upper surface and a vertical side with respect to said lower surface, comprising:

a substrate having a first surface with a first plurality of contact pads and a second plurality of contact pads, a second surface with a plurality of connection pads, and a plurality of via holes connecting said first plurality of contact pads and said plurality of

connection pads; and

a buffering means and a hermetical sealing means formed integrally, disposed substantially between said substrate and said electronic device and being contacted with a corner of said electronic device associated with said lower surface, wherein said hermetical sealing means is further contacted closely with said vertical side and said buffering means is further contacted closely with said lower surface.

26. (New) A package assembly for an electronic device having a lower surface having electrodes thereon, comprising:

a substrate having a first surface with a first plurality of contact pads and a second plurality of contact pads, a second surface with a plurality of connection pads, and a plurality of via holes connecting said first plurality of contact pads and said plurality of connection pads; and

a buffer layer disposed substantially between said substrate and said lower surface of said electronic device and having an opening to expose said first plurality of contact pads, wherein said buffer layer has a first part with a first density and a second part with a second density greater than said first density.